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09417332 - GAI: 2183

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		09417332
	Filing Date		1999-10-13
	First Named Inventor	Robert Bedichek	
	Art Unit	2183	
	Examiner Name	Ellis, Richard L.	
	Attorney Docket Number	TRAN-P009	

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	2	4794522		1988-12-27	Simpson	
	3	5167023		1992-11-24	de Nicolas, et al.	
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9	5668969		1997-09-16	Fitch	
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11	5721927		1998-02-24	Baraz, et al.	
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4	Ung et al.; "DYNAMIC RE-ENGINEERING OF BINARY CODE WITH RUN-TIME FEEDBACKS;" Department of Computer Science and Electrical Engineering, University of Queensland, QLD, Australia; 2000 (Tran291)	<input type="checkbox"/>
5	Ung et al.; "MACHINE-ADAPTABLE DYNAMIC BINARY TRANSLATION;" Proceedings of the ACM Sigplan Workshop on Dynamic and Adapative Compilation and Optimization, Jan. 2000 pgs 30-40 (Tran291)	<input type="checkbox"/>
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